EEE final syllabus

**I LOB U** :3

Reply: No lob u -.-

* Lecture 8
* **Digital Design (Chapter 7) (** Article: 7.5, 7.6, 7.7)
* <https://drive.google.com/file/d/1lQK1T4eMRw5FhrHqbAotcQoeDrc1ZvG6/view>
* **Fundamentals of Digital Logic (** Appendix B.6- B.6.1, B.6.2)
* <https://drive.google.com/file/d/1b-WrBS-xf3yNLkztxFmebTeOqI5bVTAy/view>
* [~~slides of lecture 8~~](https://drive.google.com/file/d/1YJOlqyqnRUO-lCLj6SqzihLv14c6JCA8/view)
* Lecture 7
* **Digital Fundamentals (Chapter 10) (** Article: 10.1, 10.2, 10.3, 10.4)<https://drive.google.com/file/d/1PwuMFRUAnfR694Dq001wN0f1CaSC8_Gl/view?usp=drive_web&authuser=1>
* **Digital Integrated Circuits (Chapter 12)** (Article: 12.2.1, 12.2.3)<https://drive.google.com/file/d/1BzLMo_0I2lByUFh2r8HxjkLHcni69bGt/view?usp=drive_web&authuser=1>
* [lecture 7 slides](https://drive.google.com/file/d/1pw-DtieRD44zNaTDGhi8JRMXAZ6VsQ0z/view?usp=drive_web&authuser=1)
* ~~Lecture 6~~
* [~~lecture 6~~](https://drive.google.com/file/d/1fS_e0aTCuZxlDE7t0MD1dgnT0sgzrlYM/view?usp=drive_web&authuser=1)
* [recording of class](https://drive.google.com/file/d/1a1tf_3VxvqPdppwpQ1KS2949JQNWMUwh/view?usp=drive_web&authuser=1)

**Electronic Instruments and Instrumentation Technology (Chapter 2)**

* ~~Article: 2.2- 2.2.1, 2.2.2 (Weighted-resistor DAC, R-2R Ladder DAC)~~[~~https://drive.google.com/file/d/1fW3wjfavrlNcz5bG0R2w30F7q41FNOJz/view?usp=drive\_web&authuser=1~~](https://drive.google.com/file/d/1fW3wjfavrlNcz5bG0R2w30F7q41FNOJz/view?usp=drive_web&authuser=1)
* ~~Article: 2.3- 2.3.2 (Single Slope ADC, Dual Slope ADC, Successive Approximation ADC, Flash ADC, Sample and Hold Circuit)~~
* Lecture 5
* [slide 5](https://drive.google.com/file/d/1OoWwu6iIci_YKrAJSaxJOP1VFvwko3xk/view?usp=drive_web&authuser=1)
* **Operational Amplifiers and Linear Integrated Circuits (Chapter 6)**[**https://drive.google.com/file/d/1uETtW6ftrCvSzSrM\_j\_Z\_xKqQ6GZqPue/view?usp=drive\_web&authuser=1**](https://drive.google.com/file/d/1uETtW6ftrCvSzSrM_j_Z_xKqQ6GZqPue/view?usp=drive_web&authuser=1)

Article: 6.1, 6.2, 6.3

* **Pulse, Digital and Switching Waveforms (Chapter 9)**[**https://drive.google.com/file/d/1u46iEINEBVgJfWjmerf7XGmsH5v\_\_ZQ6/view?usp=drive\_web&authuser=1**](https://drive.google.com/file/d/1u46iEINEBVgJfWjmerf7XGmsH5v__ZQ6/view?usp=drive_web&authuser=1)

Article: 9.11, 9.12, 9.13, 9.14, 9.15

* **Digital Fundamentals (Chapter 7)**[**https://drive.google.com/file/d/1PwuMFRUAnfR694Dq001wN0f1CaSC8\_Gl/view?usp=drive\_web&authuser=1**](https://drive.google.com/file/d/1PwuMFRUAnfR694Dq001wN0f1CaSC8_Gl/view?usp=drive_web&authuser=1)

Article: 7.6

Example: 7.14, 7.15

Exercise Problem: 29, 30, 31

* Lecture 4
* Mosfet slides<https://drive.google.com/file/d/1mjRpjwwv7Flio1BiQkZxHEttriuGriba/view?usp=drive_web&authuser=1>
* Lecture 3
* Reference Articles:[Sad final prep](https://docs.google.com/document/u/1/d/1JE0ys_Ivq4mTW4SlHpXfXgkvzIleXv-Pis6rLqxYfi0/edit)[(Prentice Hall electronics and VLSI series) Jan M Rabaey\_ Anantha P Chandrakasan\_ Borivoje Nikolić, Assistant Professor - Digital integrated circuits \_ a design perspective-Pearson Education (2003).pdf](https://drive.google.com/file/d/12F7ijpyhdCOu4w8XygMWsVZFQgYFmMmf/view?usp=drive_web&authuser=1)

Art: 5.1,5.2<https://drive.google.com/file/d/1d7JuGpWOJvrTUfNGV-yyJd3wYOydu6yx/view?usp=drive_web&authuser=1>

Art: 6.1, 6.2>6.2.1

* Chapter 6 ,5

slides<https://drive.google.com/file/d/14_snynFGs9Jd7ILNbXZPkZwc4hTJntsY/view?usp=drive_web&authuser=1>

* Lecture 2
* Slides and article
* [Slide](https://drive.google.com/file/d/1tLatF3Ptn1lhRmkAZ5pWCa3fxwdS7Ygu/view?usp=drive_web&authuser=1)
* <https://drive.google.com/file/d/1tWT3v_PGjNP1QnwqhD-MOWk7eKwyL9c2/view?usp=drive_web&authuser=1>
* Lecture 1
* slides<https://drive.google.com/file/d/1owwO64VKlNWVDYRNiuqzBz_91ckIewRc/view?usp=drive_web&authuser=1>